

FIG. 1

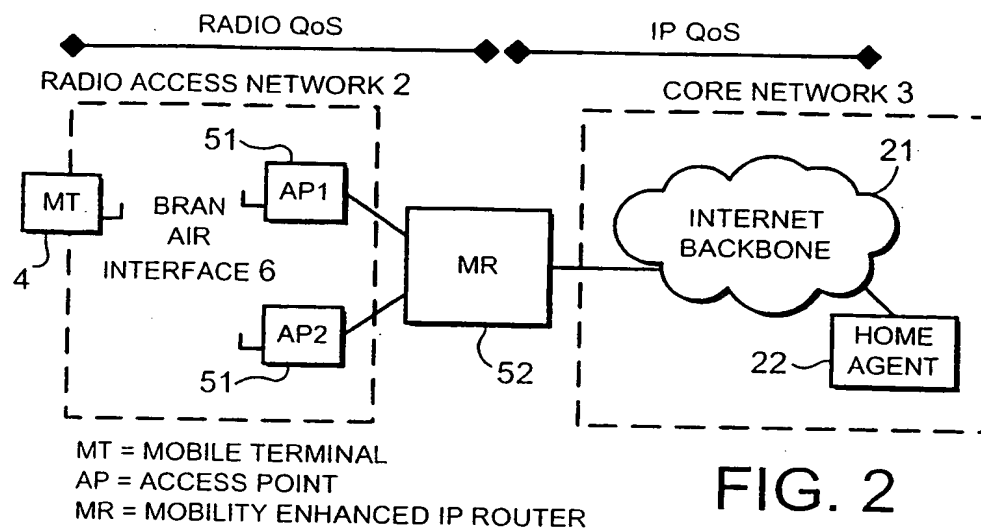


FIG. 2

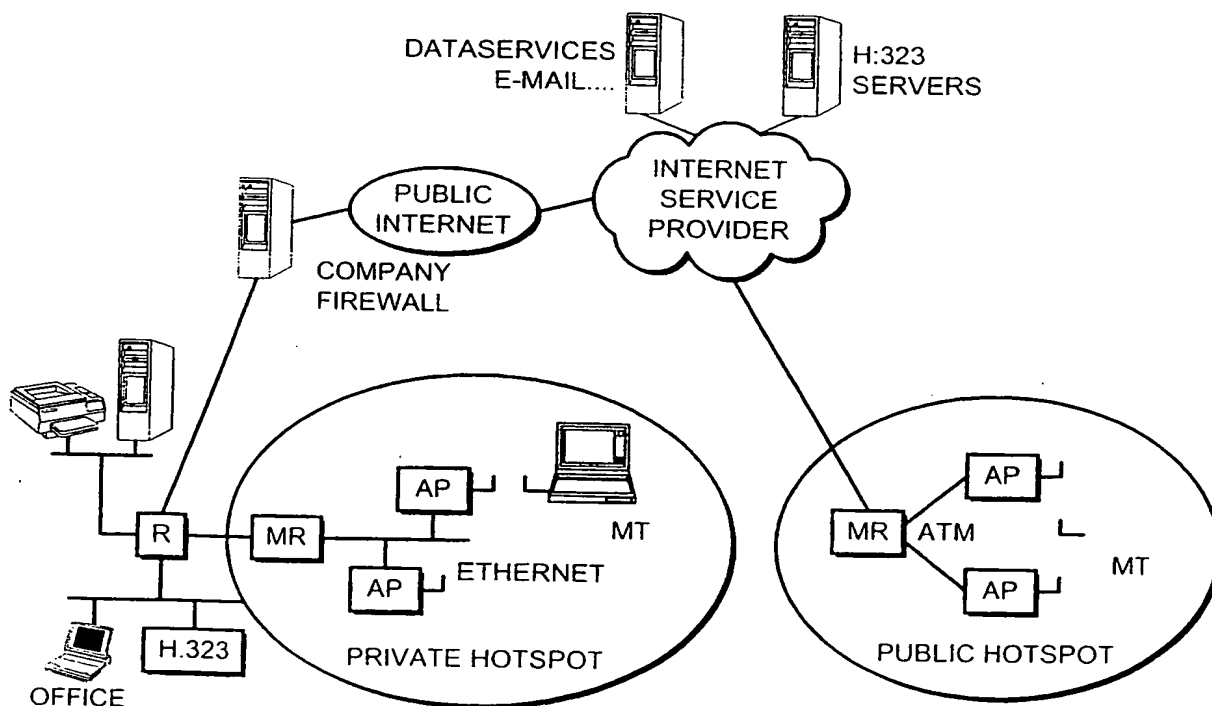


FIG. 3

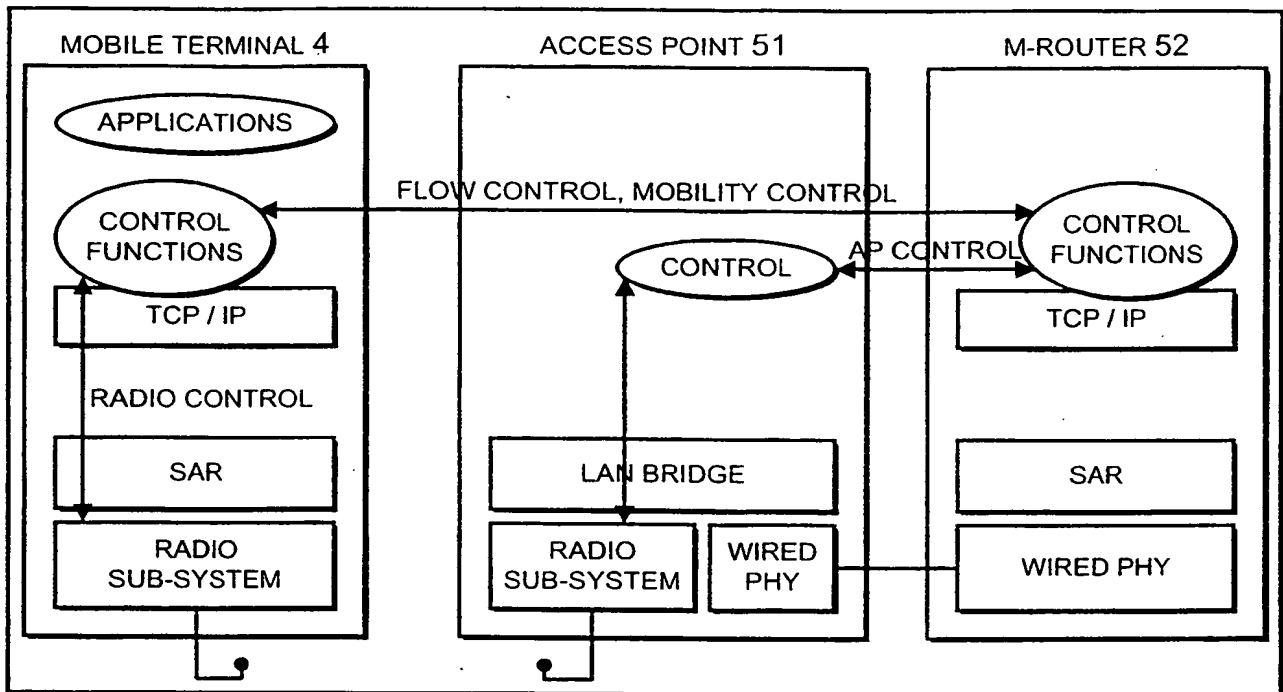
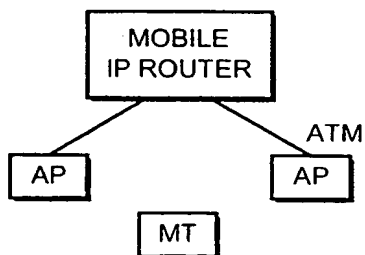


FIG. 4

TELECOM ARCHITECTURE - ATM



DATACOM ARCHITECTURE - ETHERNET

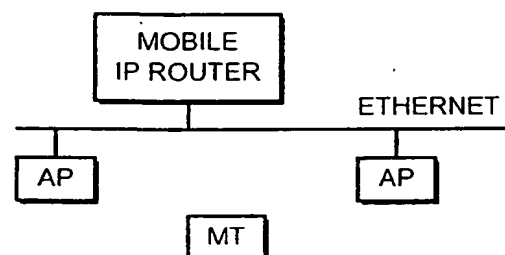


FIG. 5

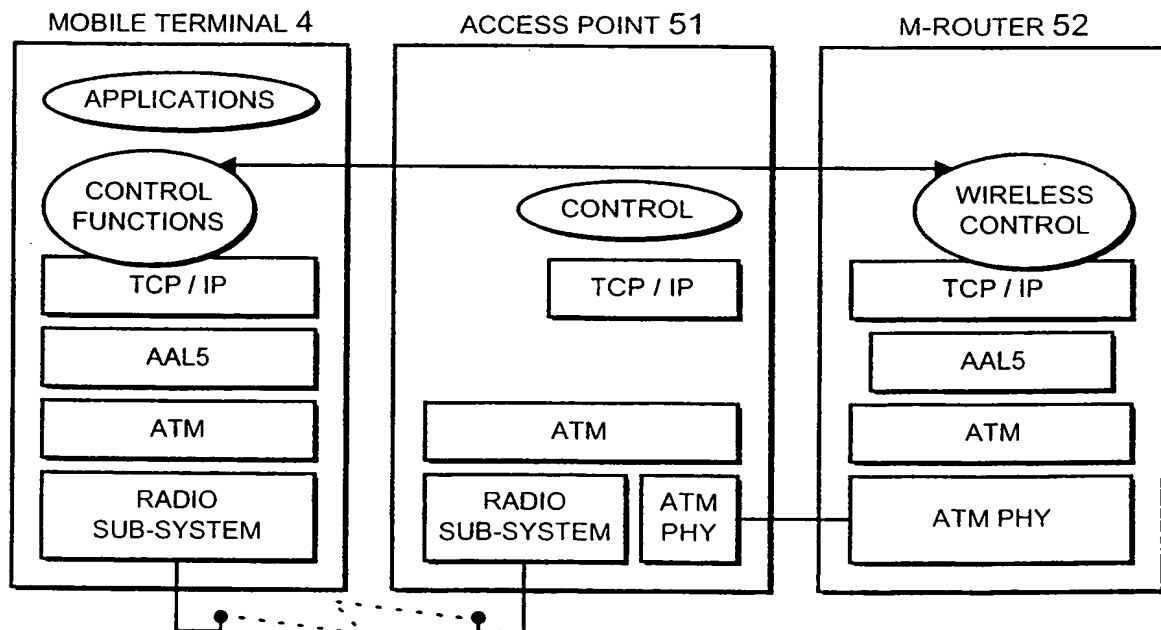
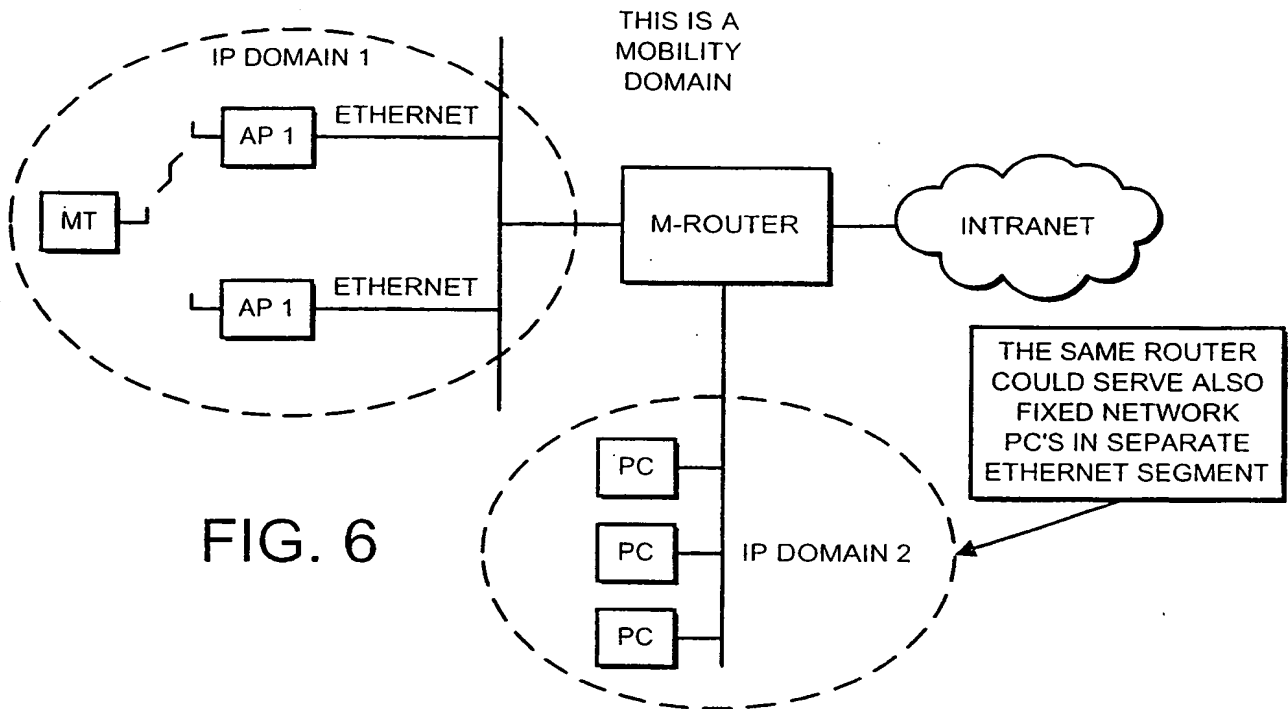


FIG. 7

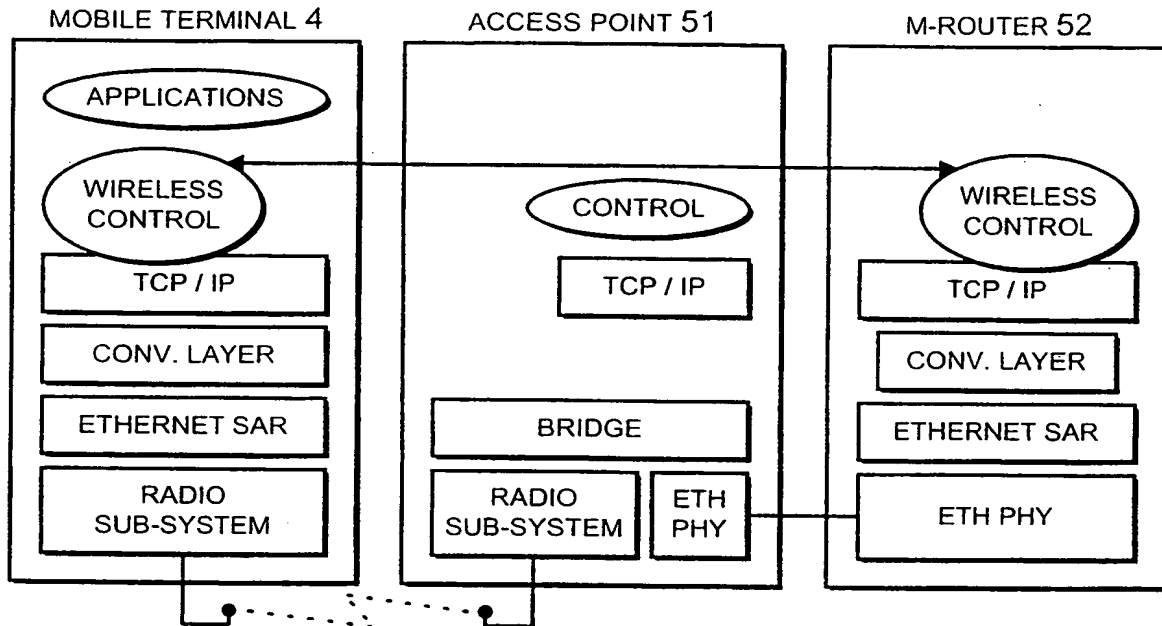


FIG. 8

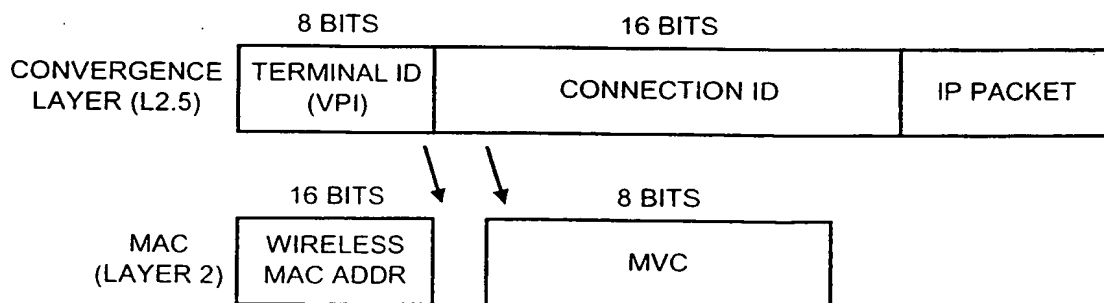


FIG. 9

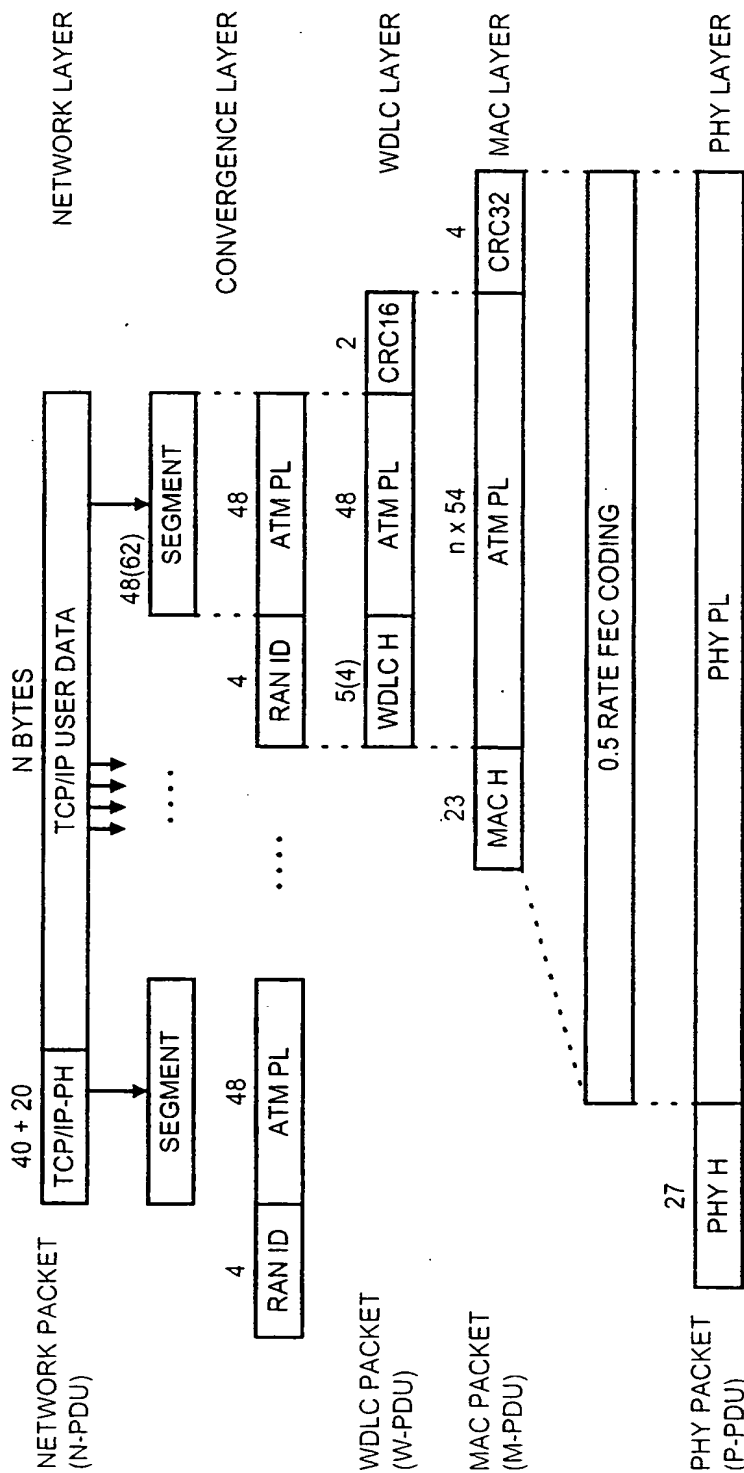
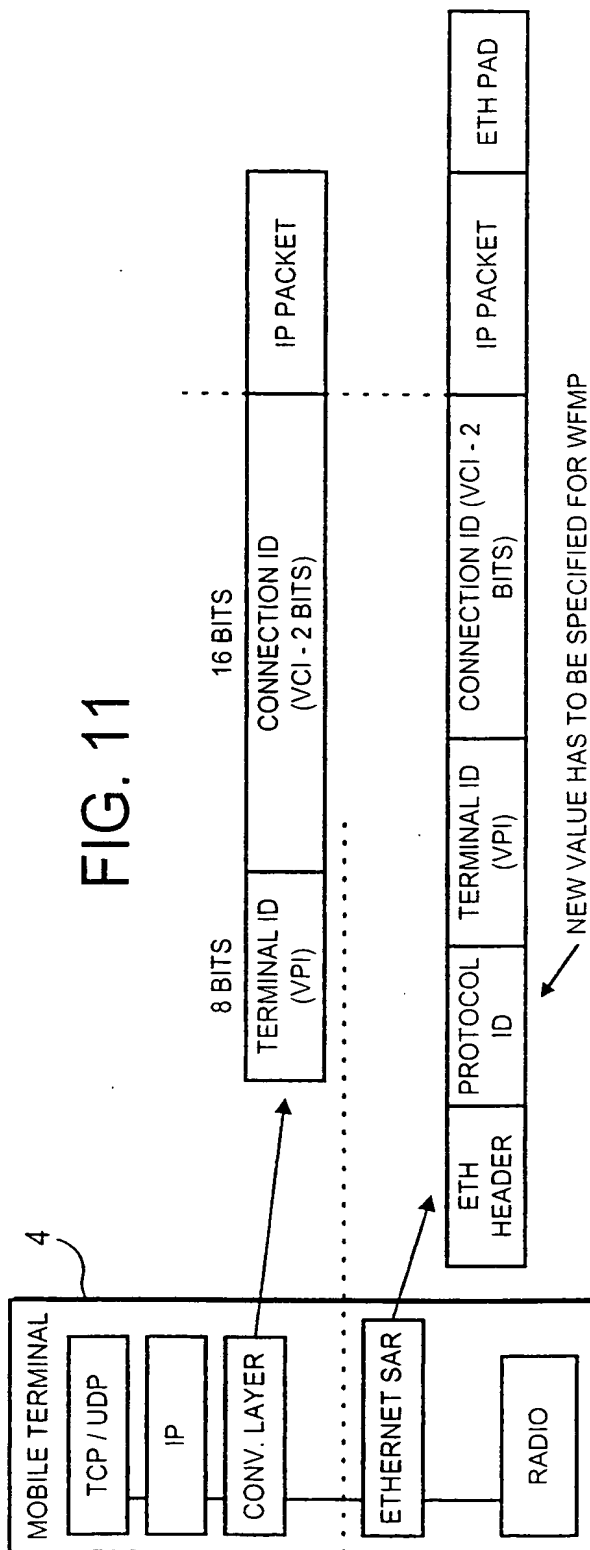


FIG. 10



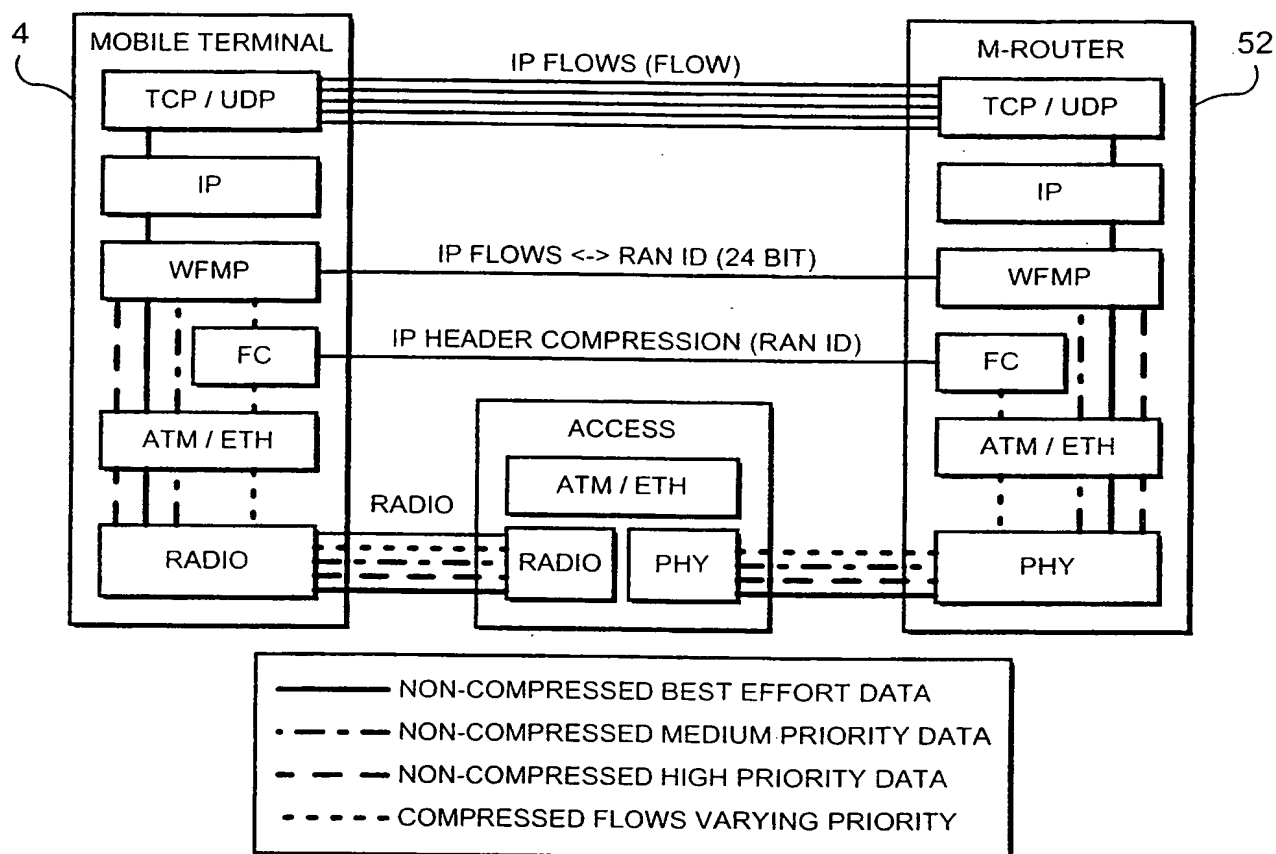
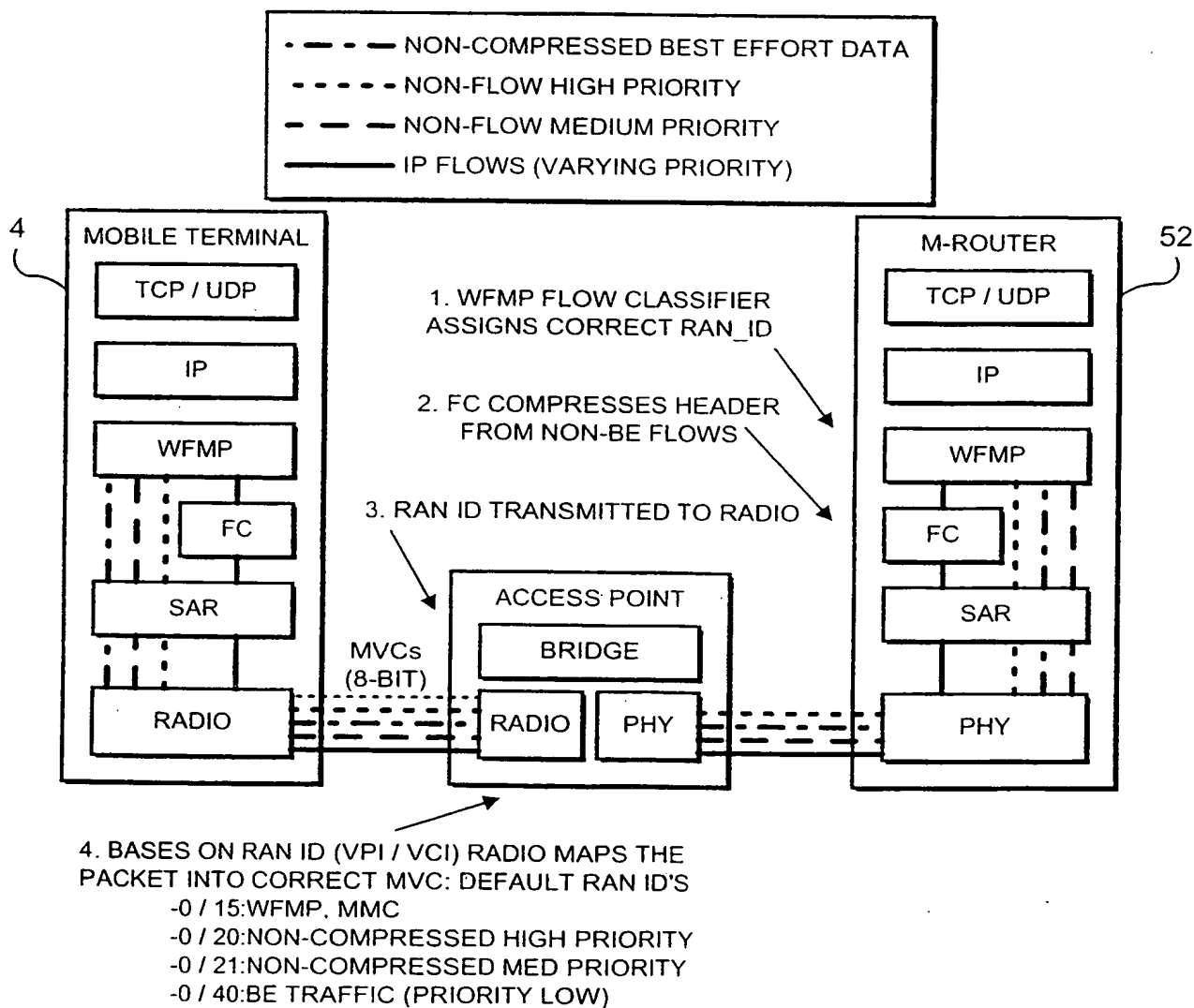


FIG. 12

9 / 18

FIG. 13



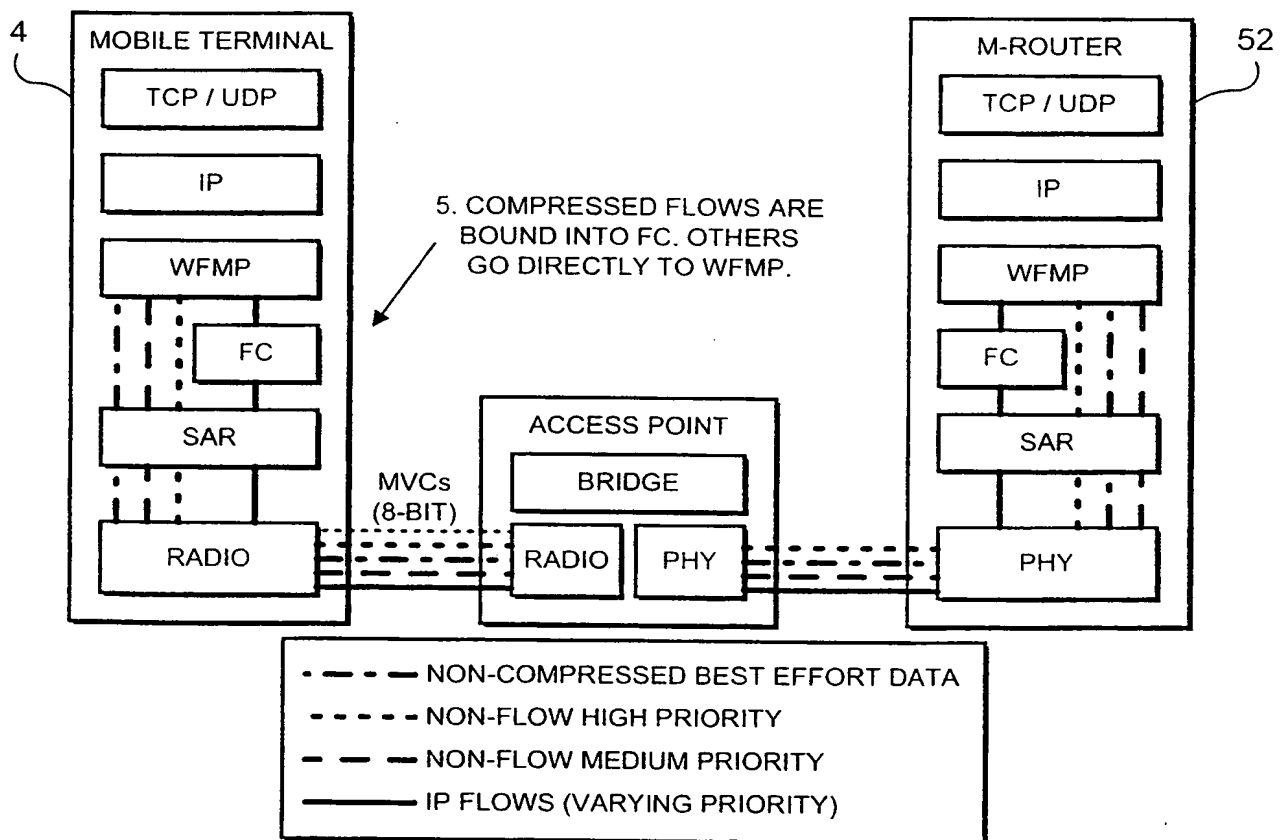


FIG. 14

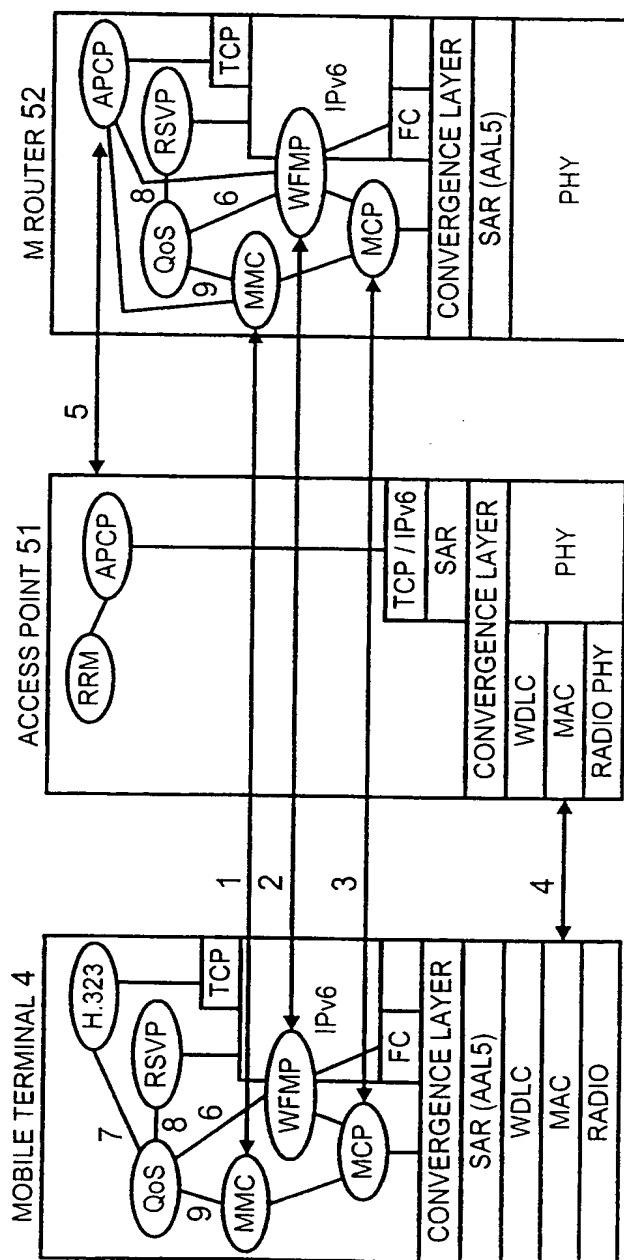
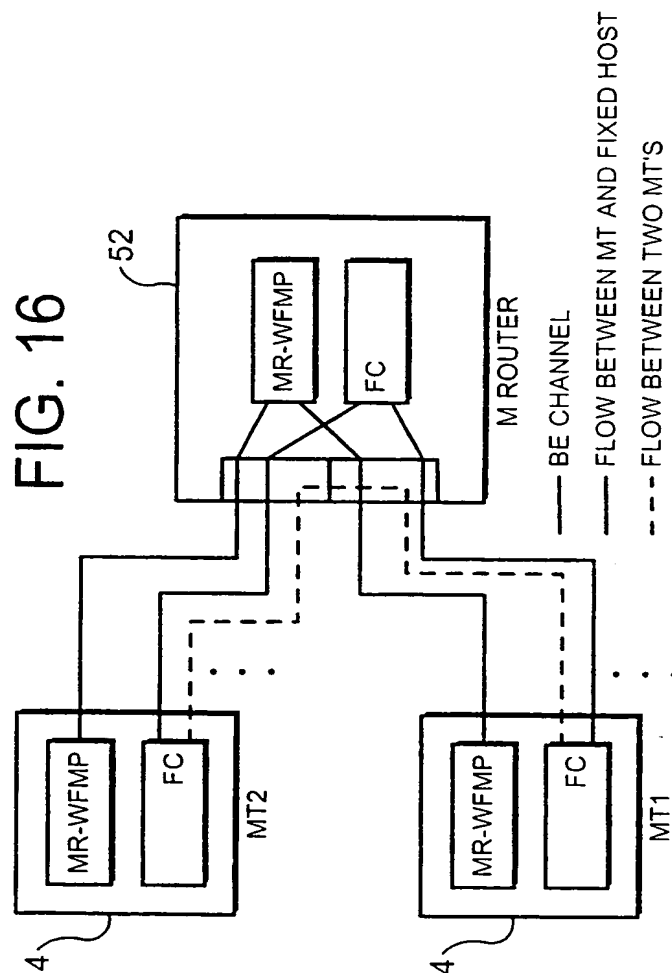


FIG. 15



ACTIVE FLOWS TABLES

FLOW_TYPE	DST_ADDR	SRC_ADDR	FLOW_LABEL	PROTOCOL	DST_PORT	SRC_PORT	RAN_ID	AP_IF

DEFAULT FLOWS TABLES

MT_id	RAN_ID_1	RAN_ID_2	RAN_ID_3	AP_IF

FIG. 17

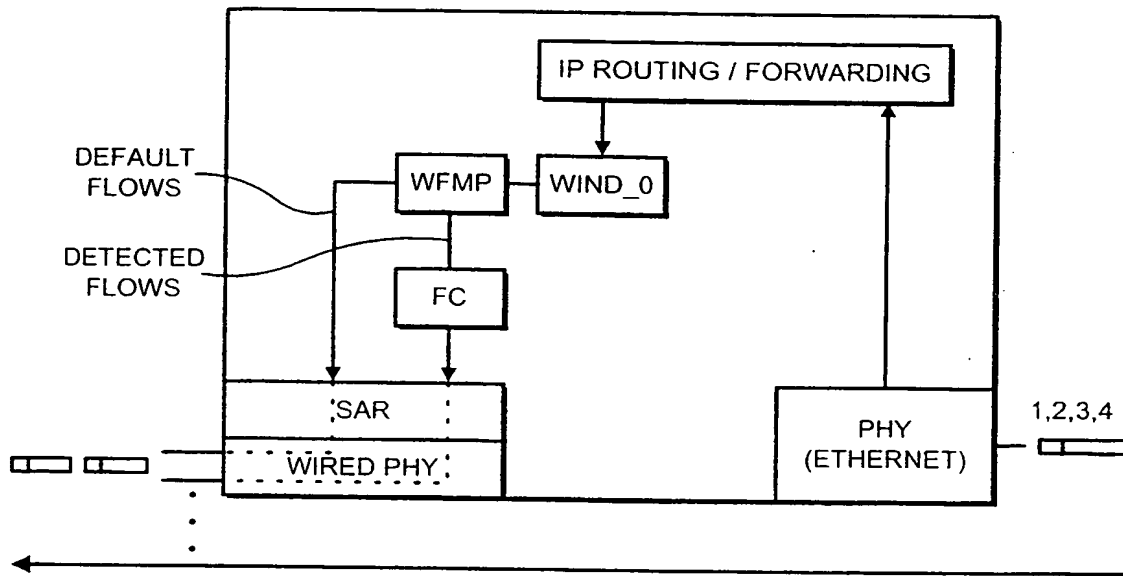


FIG. 18

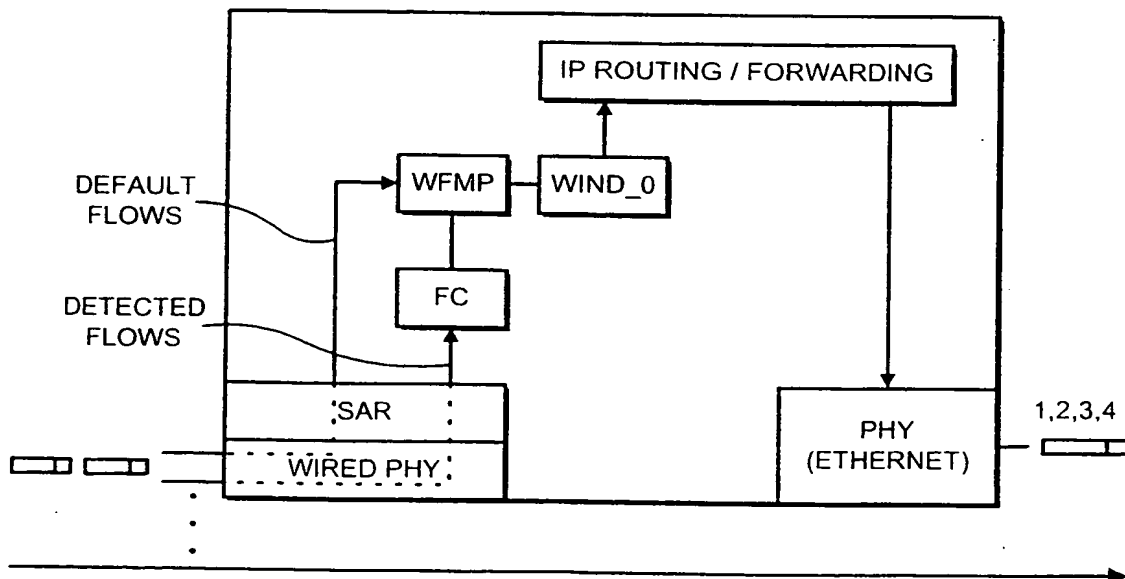


FIG. 19

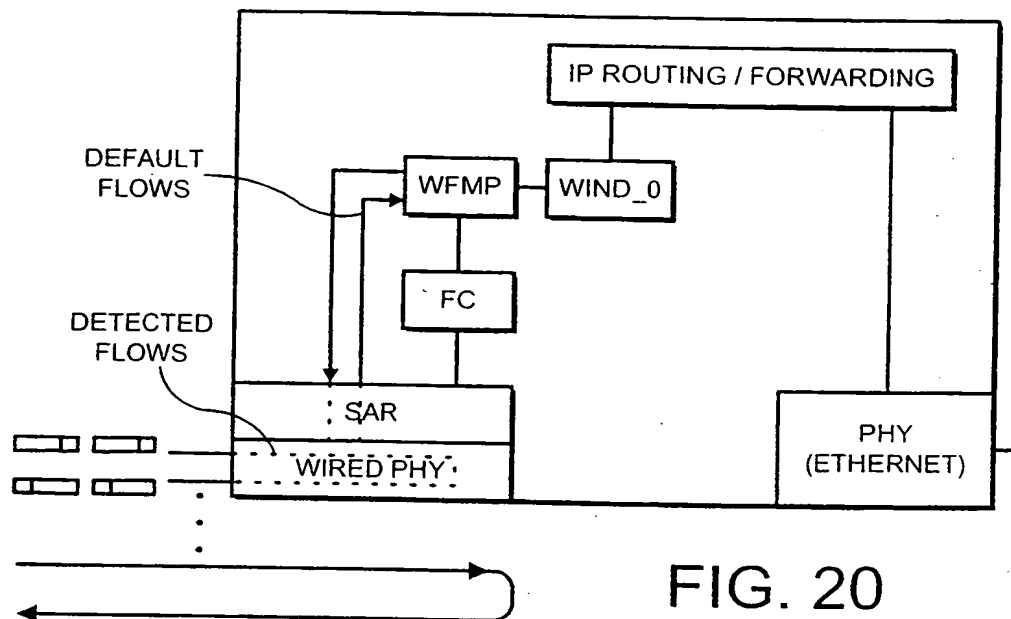


FIG. 20

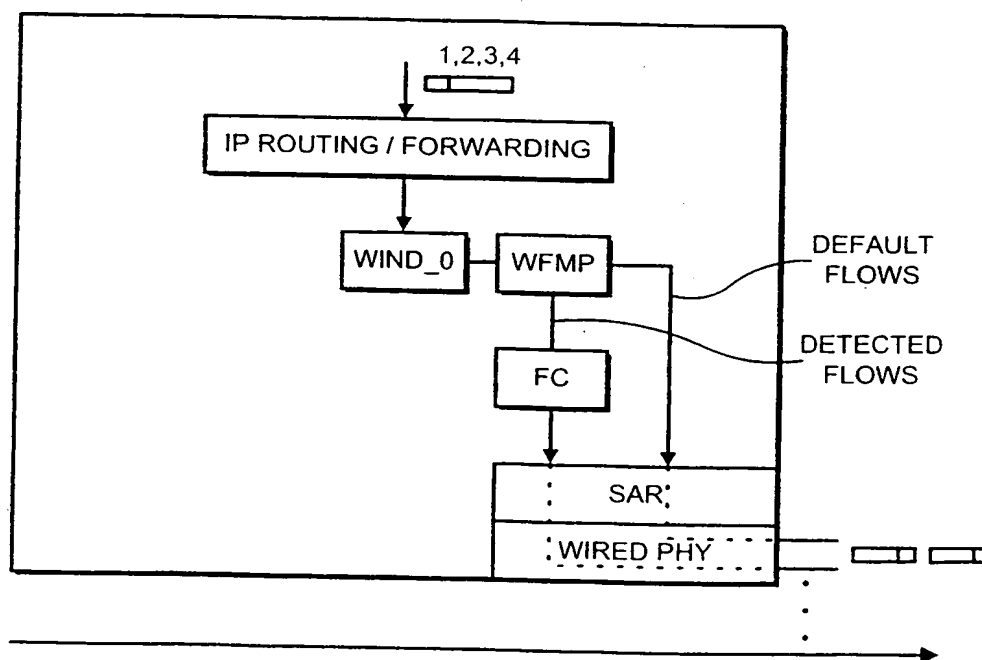


FIG. 21

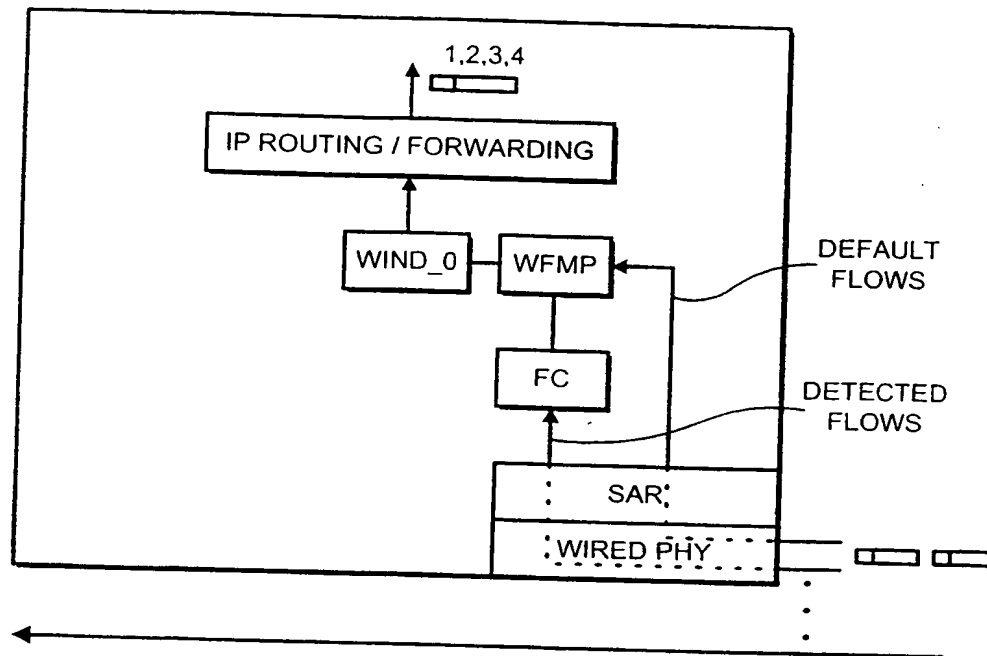


FIG. 22

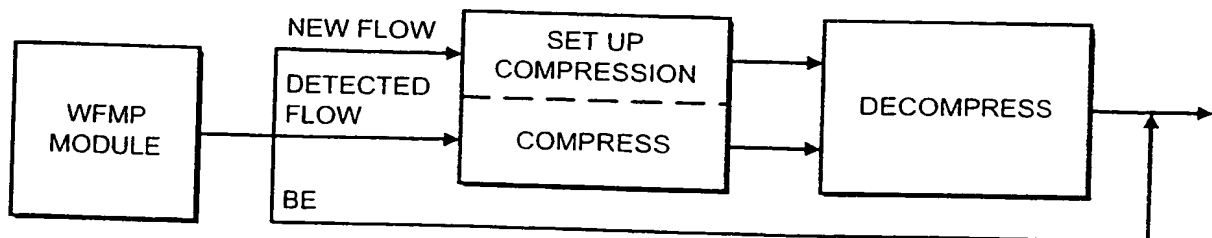
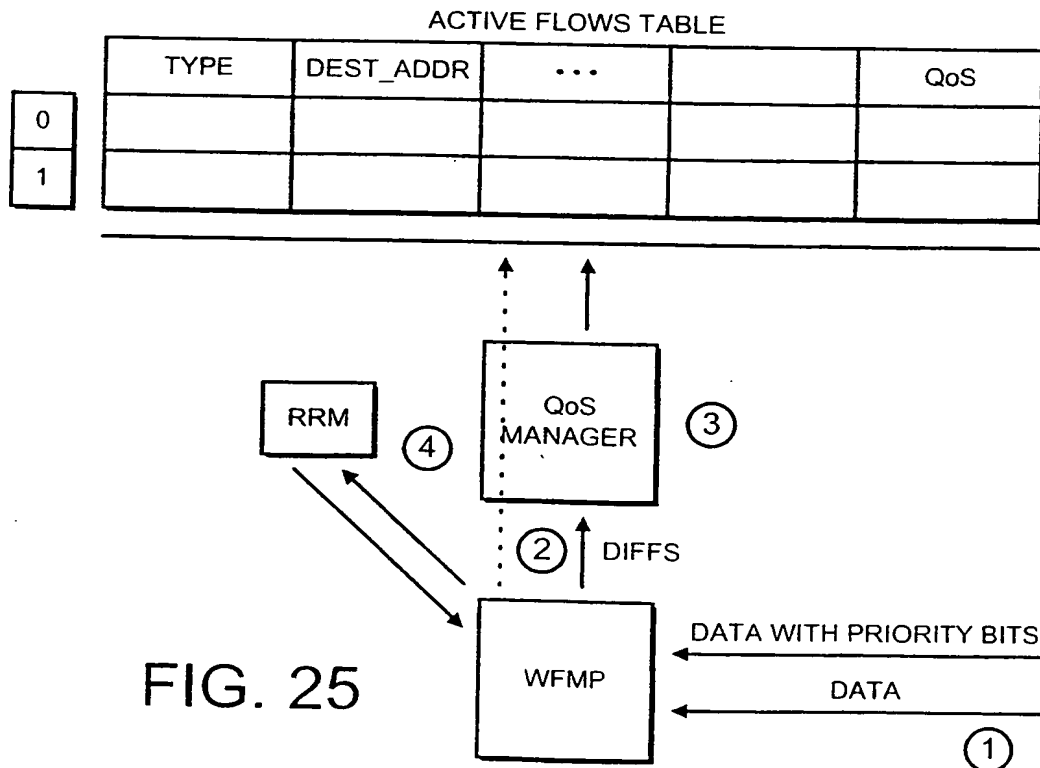
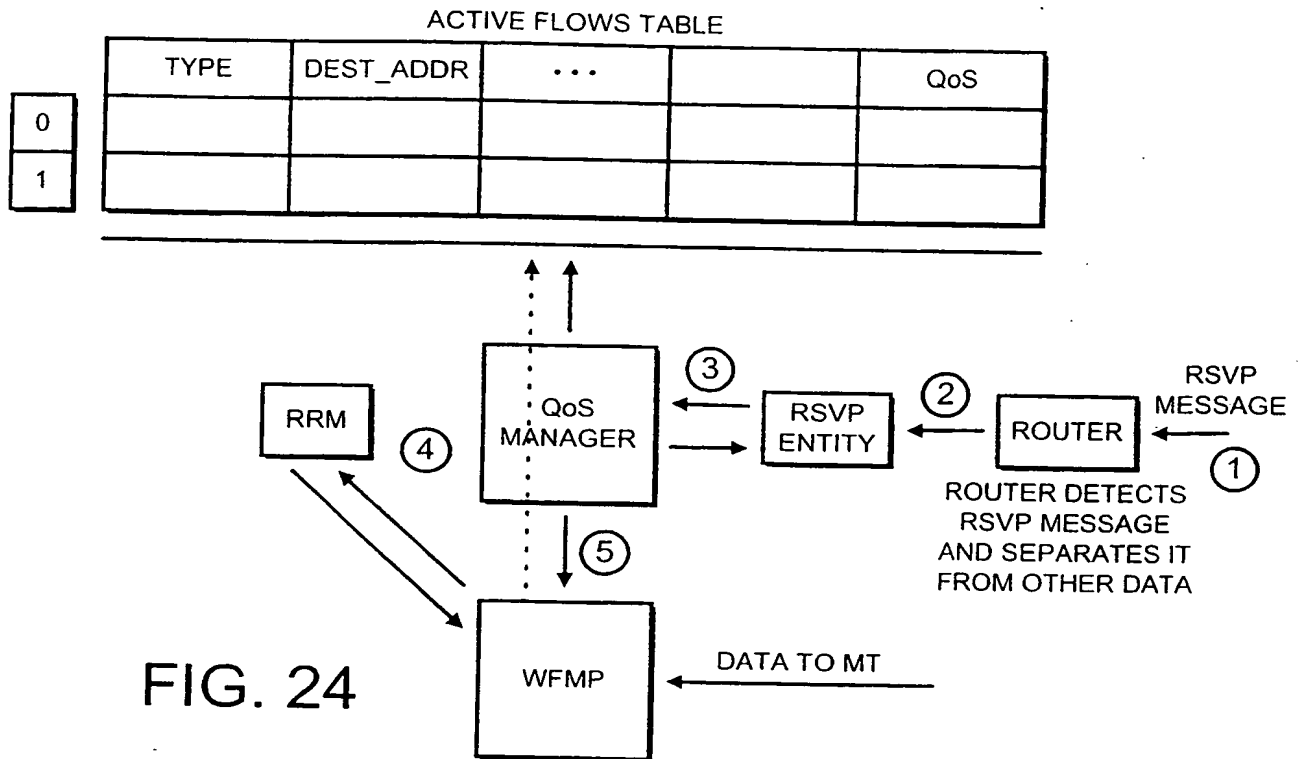
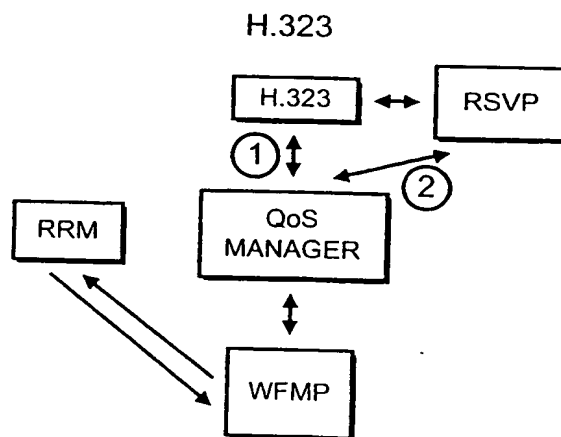
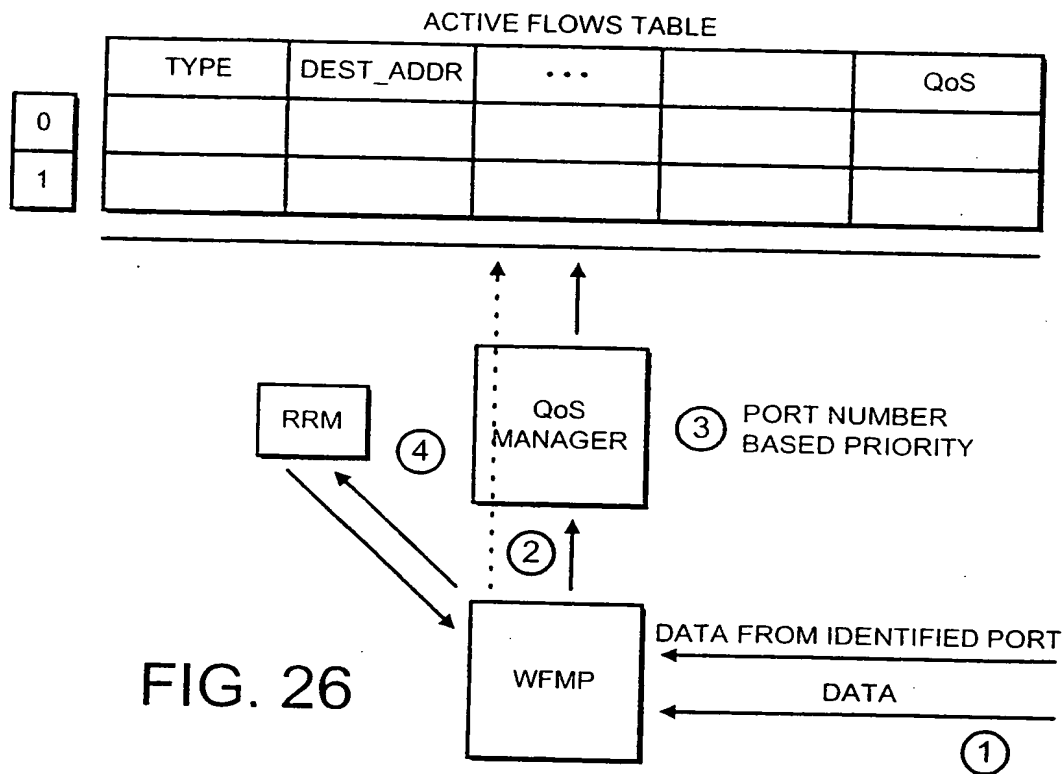


FIG. 23





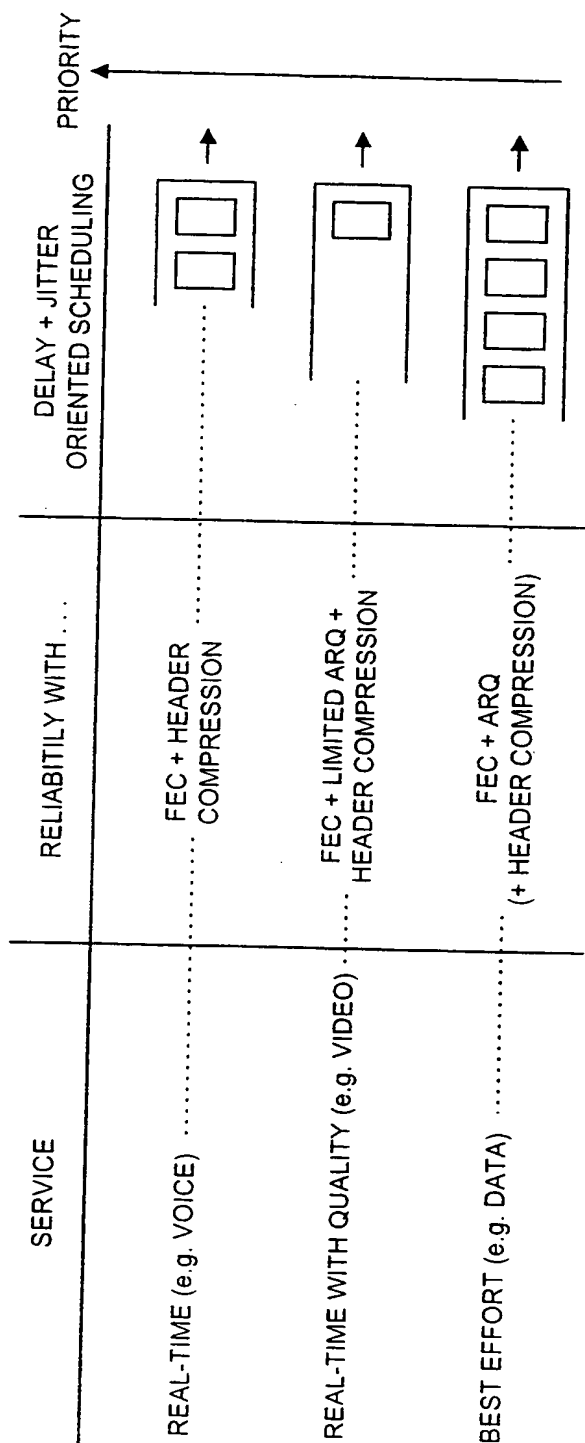


FIG. 28